FIG. 1

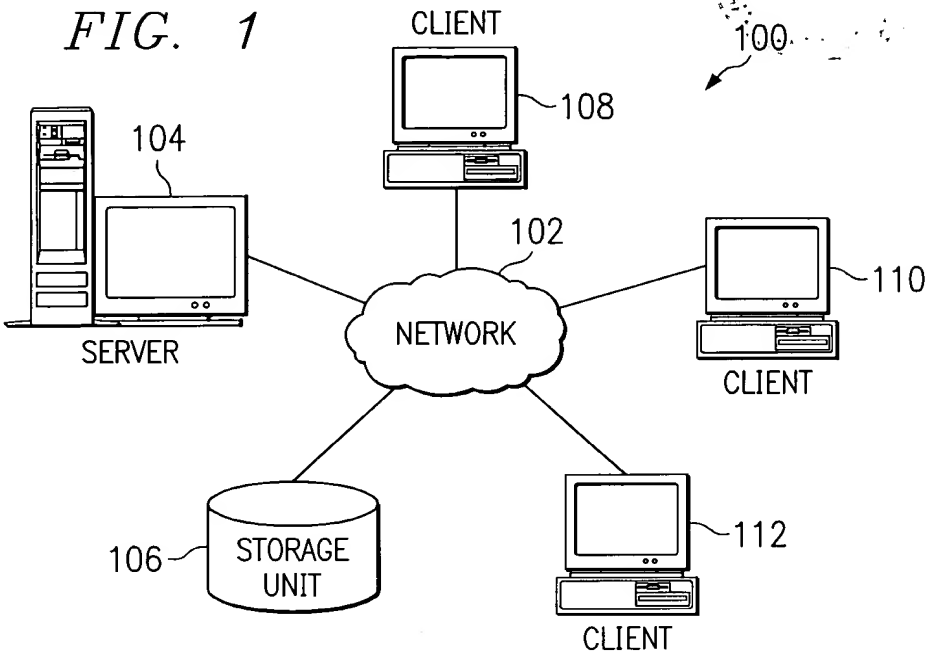
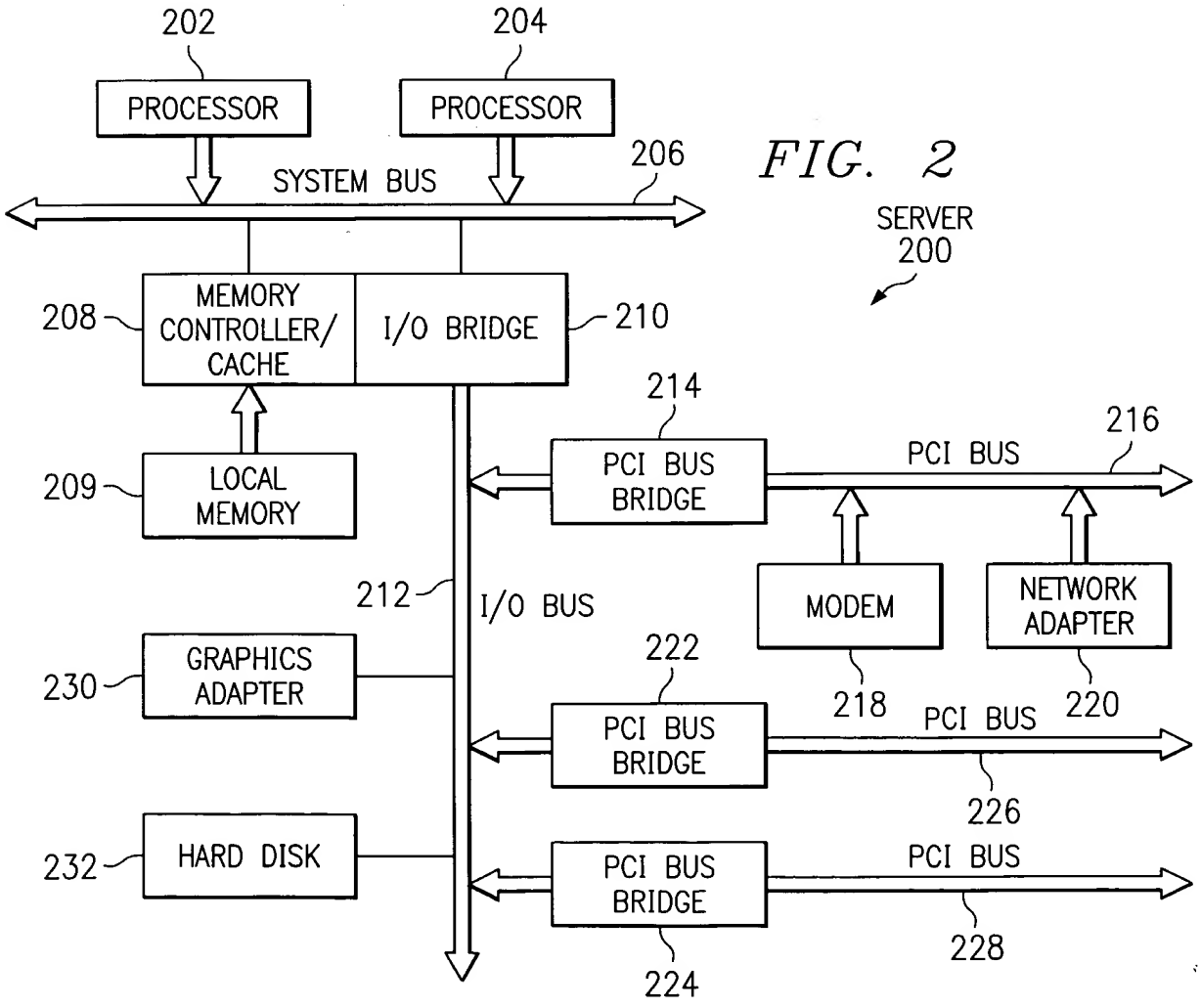

CLIENT
100
108
104
102
110
NETWORK
SERVER
CLIENT
106
STORAGE UNIT
112
CLIENT

FIG. 2


202
204
PROCESSOR
PROCESSOR
206
SYSTEM BUS
SERVER
200
208
MEMORY CONTROLLER/ CACHE
I/O BRIDGE
210
214
216
209
LOCAL MEMORY
PCI BUS BRIDGE
PCI BUS
212
I/O BUS
MODEM
NETWORK ADAPTER
222
230
GRAPHICS ADAPTER
218
PCI BUS
220
PCI BUS BRIDGE
226
232
HARD DISK
PCI BUS BRIDGE
PCI BUS
228
224

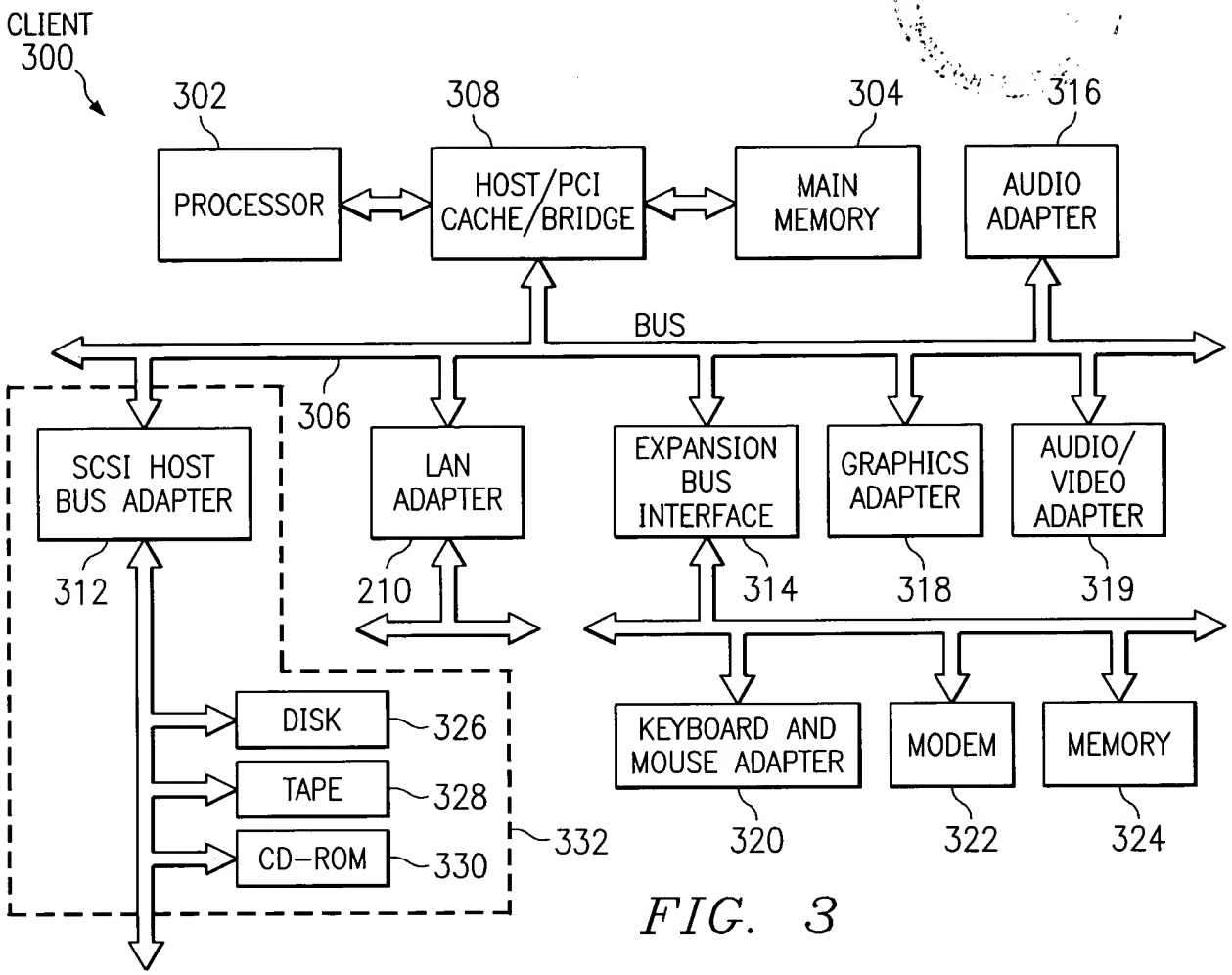

CLIENT
300
302
308
304
316
PROCESSOR
HOST/PCI
CACHE/BRIDGE
MAIN
MEMORY
AUDIO
ADAPTER
BUS
306
SCSI HOST
BUS ADAPTER
LAN
ADAPTER
EXPANSION
BUS
INTERFACE
GRAPHICS
ADAPTER
AUDIO/
VIDEO
ADAPTER
312
210
314
318
319
DISK
326
TAPE
328
CD-ROM
330
332
KEYBOARD AND
MOUSE ADAPTER
MODEM
MEMORY
320
322
324

FIG. 3

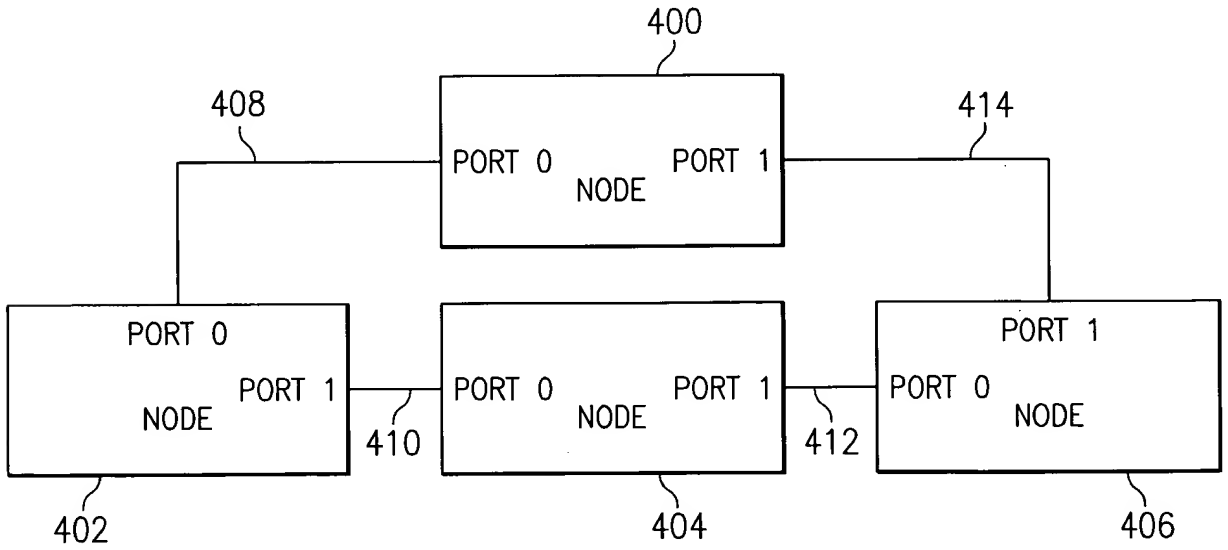

400
408
414
PORT 0
PORT 1
NODE
PORT 0
PORT 1
NODE
410
PORT 0
PORT 1
NODE
412
PORT 1
PORT 0
NODE
402
404
406

FIG. 4

FIG. 5

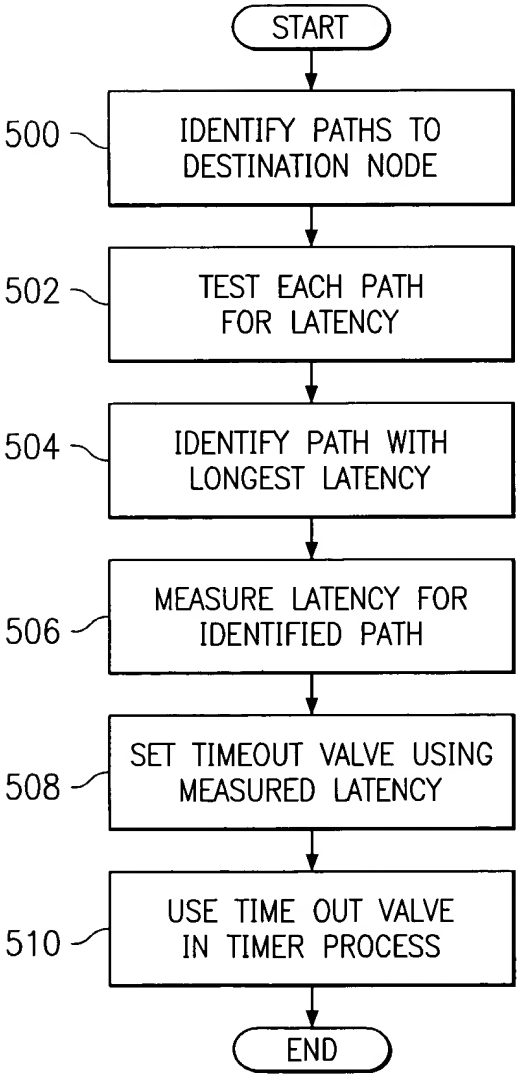
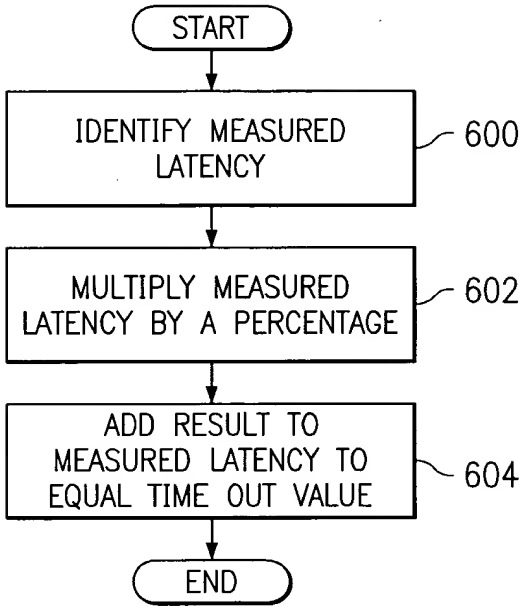

START
500
IDENTIFY PATHS TO DESTINATION NODE
502
TEST EACH PATH FOR LATENCY
504
IDENTIFY PATH WITH LONGEST LATENCY
506
MEASURE LATENCY FOR IDENTIFIED PATH
508
SET TIMEOUT VALVE USING MEASURED LATENCY
510
USE TIME OUT VALVE IN TIMER PROCESS
END

FIG. 6


START
IDENTIFY MEASURED LATENCY
600
MULTIPLY MEASURED LATENCY BY A PERCENTAGE
602
ADD RESULT TO MEASURED LATENCY TO EQUAL TIME OUT VALUE
604
END